



Visvesvaraya Technological University, Belgaum Karnataka State, INDIA 35036

TRANSCRIPT AS PER RECORDS

We do not have GPA scheme of evaluation

Name : MALLIKARJUN
University Seat No : 4NI06EC025
Year of Entrance : 2006
Year of Leaving : 2010
Degree Received : Bachelor of Engineering
(Electronics & Communication Engineering)

1. Duration of the Course : 4 Years
2. Medium of Instruction : English
3. First class with distinction (FCD) : Not less than 70% of the aggregate marks in first attempt
4. First class (FC) : Less than 70% but not less than 60% of the aggregate marks in first attempt
5. Second class (SC) : Less than 60% of the aggregate marks in first attempt

SUBJECTS	Hours Per Week		Marks Obtained	Max. Marks
	Lecture	Drawing/Practical		
I Semester				
1 Engineering Maths - I	4		105	125
2 Engineering Physics	4		96	125
3 Elements of Civil Engg. & Engg. Mechanics	4		110	125
4 Elements of Mechanical Engg.	4		73	125
5 Basic Electrical Engg.	4		90	125
6 Workshop Practice		3	50	75
7 Engg. Physics Lab		3	66	75
8 Constitution of India & Professional Ethics	2		46	75
First Attempt Total: 590 / 775 ; Class : FCD ; # 1				
II Semester				
1 Engineering Maths - II	4		111	125
2 Engineering Chemistry	4		100	125
3 Computer Concepts & C Programming	4		98	125
4 Computer Aided Engineering Drawing	2	4	114	125
5 Basic Electronics	4		81	125
6 Computer Programming Lab		3	70	75
7 Engg. Chemistry Lab		3	57	75
8 Environmental Studies	2		49	75
First Attempt Total: 631 / 775 ; Class : FCD ; # 1				
III Semester				
1 Engineering Mathematics -III	4		114	125
2 Analog Electronic Circuits	4		81	125
3 Logic Design	4		93	125
4 Network Analysis	4		62	125
5 Electronic Instrumentation	4		82	125
6 Field Theory	4		94	125
7 Analog Electronics Lab		3	61	75
8 Logic Design Lab		3	63	75
First Attempt Total: 650 / 900 ; Class : FCD ; # 1				
IV Semester				
1 Engineering Mathematics - IV	4		87	125
2 Microcontrollers	4		82	125
3 Control Systems	4		101	125
4 Signals & Systems	4		93	125
5 Fundamentals of HDL	4		73	125
6 Linear IC's & Applications	4		83	125
7 Microcontrollers Lab		3	62	75
8 HDL Lab		3	61	75
First Attempt Total: 584 / 900 ; Class : FC ; # 3				

SUBJECTS	Hours Per Week		Marks Obtained	Max. Marks
	Lecture	Drawing/Practical		
V Semester				
1 Management & Entrepreneurship	4		82	125
2 Digital Signal Processing	4		88	125
3 Analog Communication	4		73	125
4 Microwaves and Radar	4		88	125
5 Digital Switching Systems	4		72	125
6 Fundamentals of CMOS VLSI	4		52	125
7 DSP Lab		3	61	75
8 Analog Communication Lab+LIC Lab		3	58	75
First Attempt Total 574 / 900 ; Class: FC ; # 1				
VI Semester				
1 Digital Communication	4		81	125
2 Microprocessors	4		102	125
3 Analog and Mixed Mode VLSI Design	4		86	125
4 Antennas and Propagation	4		65	125
5 Information Theory and Coding	4		94	125
6 Programming in C++	4		85	125
7 Advanced Communication Lab		3	60	75
8 Microprocessors Lab		3	70	75
First Attempt Total 643 / 900 ; Class: FCD ; # 1				
VII Semester				
1 Computer Communication Networks	4		81	125
2 Optical Fiber Communication	4		86	125
3 Power Electronics	4		91	125
4 DSP Algorithms & Architecture	4		111	125
5 Image Processing	4		82	125
6 Data Structures using C++	4		84	125
7 VLSI Lab		3	68	75
8 Power Electronics Lab		3	57	75
First Attempt Total 660 / 900 ; Class: FCD ; # 1				
VIII Semester				
1 Wireless Communication	4		80	125
2 Embedded System Design	4		93	125
3 Network Security	4		93	125
4 Multimedia Communication	4		88	125
5 Project Work		6	180	200
6 Seminar	3		41	50
First Attempt Total 575 / 750 ; Class: FCD ; # 1				
Grand total of V to VIII Semester : 2452 out of 3450 (max.)				

Class of the Degree * First Class with Distinction

06CIP/06CIV18/28 is not considered for Grand Total and the Class Declaration

AUTHENTIC



(Signature)
Registrar (Evaluation)